

## Annexure - 1

**PROFORMA FOR MANDATORY DISCLOSURE OF INFORMATION ABOUT ACCREDITED PROGRAMMES BY INSTITUTIONS TO BE INCLUDED IN THEIR RESPECTIVE INFORMATION BROCHURE, DISPLAYED ON THEIR WEBSITE AND TO BE SUBMITTED TO NBA EVERY YEAR WELL BEFORE THE ADMISSION PROCESS**

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The following information is to be published in the Information Brochure besides being hosted on the Institution's official Website as fixed content.

Name of the Programme : M. Tech. (Electronics Design & Tech.)

Approved intake : 18

Fee Charged (per annum) in Rs. : 36,000/=

Pass percentage during the last 5 years : i) 2000-01 - 92%  
ii) 2001-02 - 100%  
iii) 2002-04 - 70%

Teacher Student Ratio : 1 : 6

No. of Faculty Members :

**Cadre** **Number**

Professor : 01

Asstt. Professor : 01

Sr. Lecturer : 03

Lecturer : 01

Faculty Profile :

**Qualification** **No. of Faculty**

Ph. D. : 01

Post Graduate : 05

Under Graduate :

Experience of Faculty :

**Number of Faculty with**

Less than 5 years :

5 to 10 years : 01

More than 10 years : 05


## Details of individual faculty members :

(1)

01.	Name	Dr. A.K.D. Dwivedi	
02.	Date of Birth	01.05.1963	
03.	Educational Qualification	M. Tech., Ph.D.	
04.	Work Experience		
	- Teaching	20 Yrs.	
	- Research	--	
	- Industry	--	
	- Others	--	
05.	Area of Specializations	Digital Signal Processing	
06.	Subjects teaching at Under Graduate Level Post Graduate Level		Data Communication, EMC and Reliability, Digital Signal Processing OPTO Electronics,
07.	Research guidance	--	
	Masters's		No. of papers published in
	Ph.D.		- National Journals
			- International Conf. - 02
			- Conferences - 10
08.	Projects Carried out	--	
09.	Patents	--	
10.	Technology Transfer	--	
11.	Research Publications	--	
12.	No. of Books published with details	--	



(2)

1. Name	Sh. Nishant Tripathi	
2. Date of Birth	15.12.1980	
3. Educational Qualification	M. Tech. (Digital Electronics)	
4. Work Experience		
- Teaching	04 Yrs.	
- Research	--	
- Industry	--	
- Others	--	
5. Area of Specializations	Electronics	
6. Subjects teaching at Under Graduate Level Post Graduate Level	Digital System Design, ASIC Design Microcomputer System Design, VHDL based Design, VLSI Design Embedded System Design	
7. Research guidance	No. of papers published in	
Masters's	- National Journals --	
Ph.D.	- International Journals --	
	- Conferences --	
8. Projects Carried out	Sponsored - --	
9. Patents	--	
10. Technology Transfer	--	
11. Research Publications	--	
12. No. of Books published with details	--	

(3)

01. Name Sh. S. K. Singh
02. Date of Birth 15.09.1965
03. Educational Qualification M. E. (Control And Instrumentation)
04. Work Experience
  - Teaching 16 Yrs.
  - Research --
  - Industry 02 Yrs.
  - Others --
05. Area of Specializations Power Electronics, Control & Instr.
06. Subjects teaching at Under Graduate Level  
Post Graduate Level  
Designing with Power Devices, Physical Design of Electronic Equipment, Transducers and Instr, Data Acquisition System.
07. Research guidance
  - Masters's --
    - No. of papers published in National Journals
  - Ph.D. --
    - International Journals
    - Conferences
08. Projects Carried out --
09. Patents --
10. Technology Transfer --
11. Research Publications --
12. No. of Books published with details --



(4)

1. Name Sh. Pankaj Shukla
2. Date of Birth 01.05.1975
3. Educational Qualification M. Tech. Computer Sc.
4. Work Experience
  - Teaching 08 Yrs.
  - Research --
  - Industry --
  - Others --
5. Area of Specializations Computer Engg.
6. Subjects teaching at
  - Under Graduate Level
  - Post Graduate Level
7. Research guidance --
- Masters's --
- Ph.D. --
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8. Projects Carried out --
9. Patents --
10. Technology Transfer --
11. Research Publications --
12. No. of Books published --  
with details



Data Communication, Software for Industrial Microcomputer System, Network Programming, Computer Communication Networks.

No. of papers published in  
- National Journals

- International Journals

- Conferences

(5)

01.	Name	Sh. Alok Tripathi	
02.	Date of Birth	09.01.1969	
03.	Educational Qualification	M. Tech. (Infor. Tech)	
04.	Work Experience		
	- Teaching	10 yrs.	
	- Research	--	
	- Industry	--	
	- Others	--	
05.	Area of Specializations:	Information Technology	
06.	Subjects teaching at Under Graduate Level Post Graduate Level		Industrial Design of Electronic Equipment, Wireless Communication Software for Industrial Microcomputer System.
07.	Research guidance	--	
	Masters's	--	No. of papers published in - National Journals
	Ph.D.	--	- International Journals
		--	- Conferences
		--	
08.	Projects Carried out	--	
09.	Patents	--	
10.	Technology Transfer	--	
11.	Research Publications	--	
12.	No. of Books published with details	--	



**Industrial Consultancy, Research and Development (during the past 5 years):**

Amount of industrial Consultancy earned (in lakhs of Rs.)	: --
No. of sponsored research projects completed	: Three
No. of patents produced	: --
No. of sponsored research projects in progress	: Three
No. of paper published in International journals	: 01
No. of paper published in National journals	: --
No. of papers presented in International Conference/Seminar:	--
No. of papers presented in National Conference/Seminars	: 20
No. of International Conferences Conducted	: --
No. of National Conferences Conducted	: 01
No. of Training Programmes/Short Term Courses conducted	: 50

**Placement Record for the previous year :**

No. of Companies who visited the Campus	: --
Names and Details of companies who visited the campus Along with No. of Students placed	: --
No. of Students placed through campus recruitment	: --
Maximum Salary drawn (in Rs.)	: Rs. 5.0 lakhs
Average Salary drawn (in Rs.)	: Rs. 2.5 lakhs

**Budget Allocation for the Programme (in Rs.) :**

**Recurring : Rs. 150 lakhs**

**Non Recurring : Rs. 65 lakhs**

**New initiatives and future vision, if any**

: It is planned to start value additional programmes in Embedded System Design/ VLSI Design/ IT Security